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This book makes all things clear about designing systems controlled by microprocessors and uses the Motorola 68000 family of microprocessors as an example. It is full of clear examples and many exercises for the student, and shows details of both the hardware and programming aspects of microprocessor system design, making it ideal for engineers who are interested in the subject.

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The Motorola 68000 is a 16/32-bit complex instruction set computer microprocessor, introduced in 1979 by Motorola Semiconductor Products Sector. The design implements a 32-bit instruction set, with 32-bit registers and a 32-bit internal data bus. The address bus is 24-bits and does not use memory segmentation, which made it popular with programmers. Internally, it uses a 16-bit data arithmetic logic unit and two more 16-bit ALUs used mostly for addresses, and has a 16-bit external data bus. For

Motorola 68000 - Wikipedia

The Third Edition of MICROPROCESSOR SYSTEMS DESIGN covers the design of systems that use Motorola's 68000 family of microprocessors (including the latest generation of 68000 chips), and addresses both hardware and software considerations. Professor Clements' emphasis is practical, providing the necessary detail to enable students to design actual, working systems.

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The 6800 ("sixty-eight hundred") is an 8-bit microprocessor designed and first manufactured by Motorola in 1974. The MC6800 microprocessor was part of the M6800 Microcomputer System that also included serial and parallel interface ICs, RAM, ROM and other support chips. A significant design feature was that the M6800 family of ICs required only a single five-volt power supply at a time when ...

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Microprocessor Systems Design: 68000 Family Hardware ...

The Motorola 68000 series (also known as 680x0, m68000, m68k, or 68k) is a family of 32-bit complex instruction set computer (CISC) microprocessors. During the 1980s and early 1990s, they were popular in personal computers and workstations and were the primary competitors of Intel 's x86 microprocessors.

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